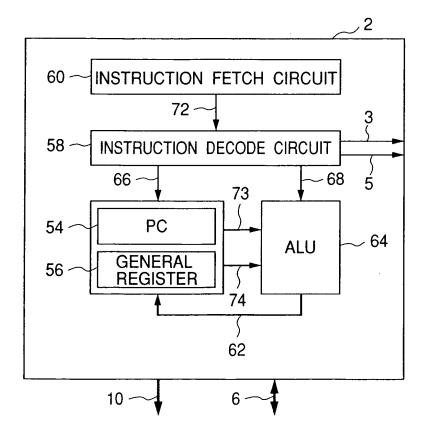
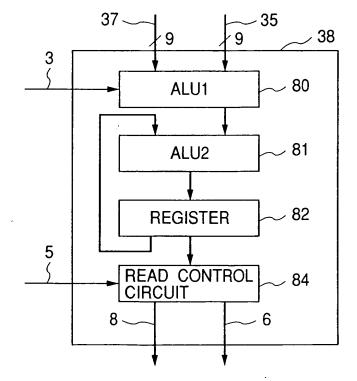


FIG. 3



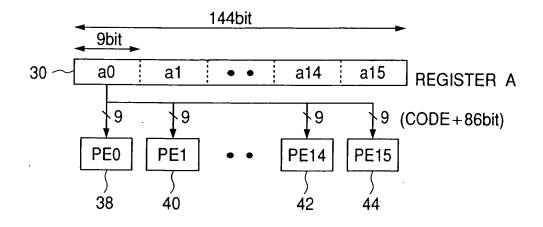
4/16

FIG. 4



100

FIG. 5



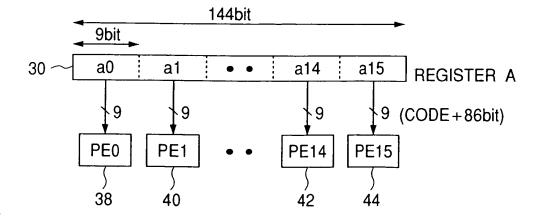
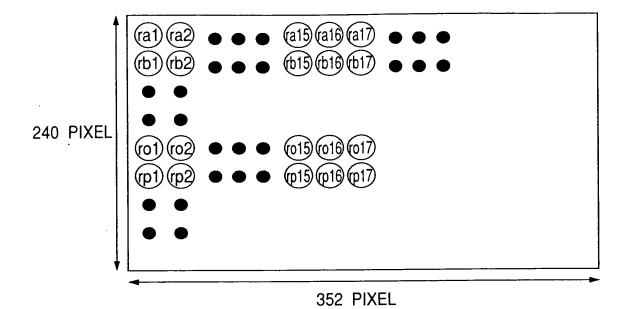


FIG. 7



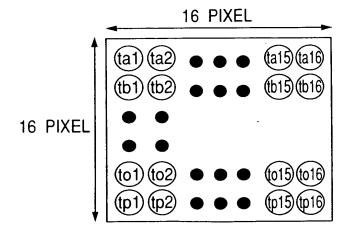
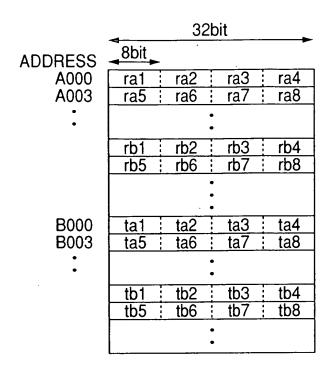
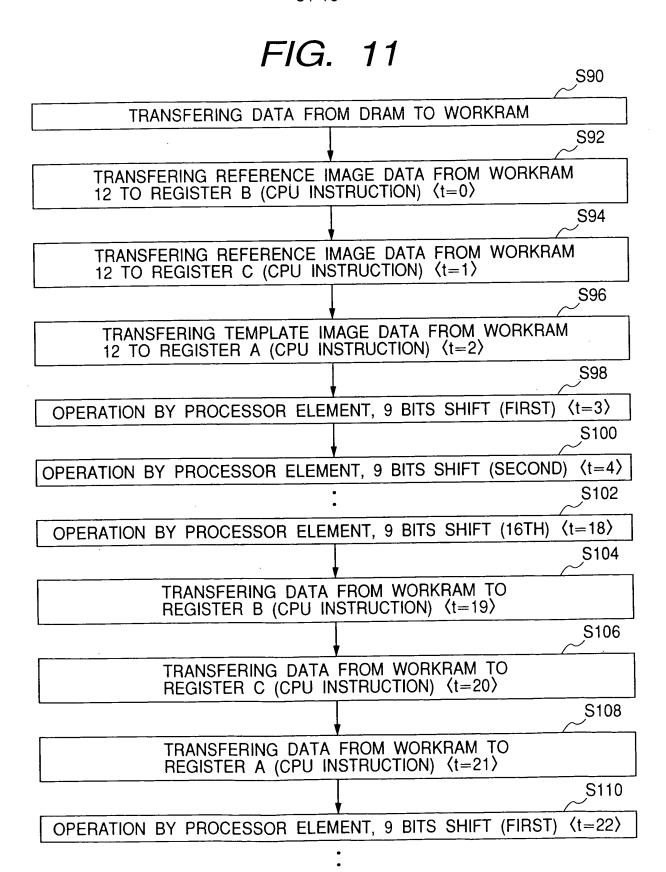


FIG. 9



	144bit
ADDRESS	9bit_
C000	ra1 ra2 ra3 ra4 . ra13 ra14 ra15 ra16
C010	ra17 : ra18 : ra19 : ra20
•	•
	rb1 rb2 rb3 rb4 rb13 rb14 rb15 rb16
	rb17 : rb18 : rb19 : rb20 rb29 : rb30 : rb31 : rb32
	•
D000	ta1 ta2 ta3 ta4 . ta13 ta14 ta15 ta16
D010	tb1 : tb2 : tb3 : tb4 tb13 : tb14 : tb15 : tb16
•	•
	to1 to2 to3 to4 . to13 to14 to15 to16 tp1 tp2 tp3 tp4 . tp13 tp14 tp15 tp16
	tp1 : tp2 : tp3 : tp4
	•



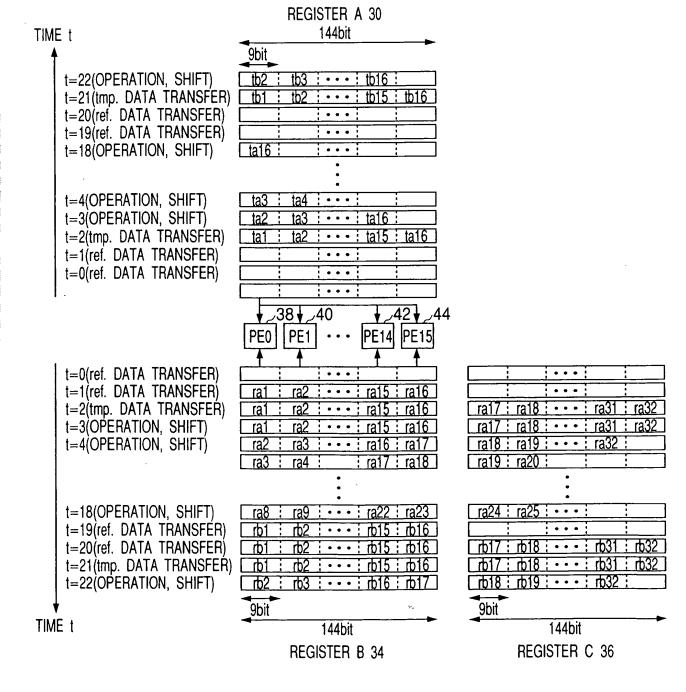
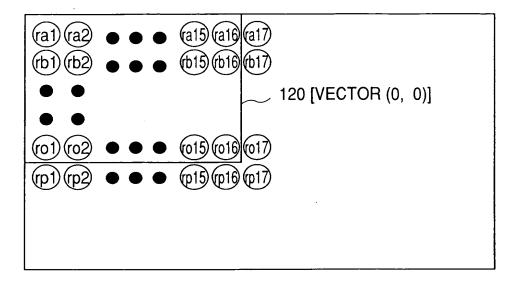
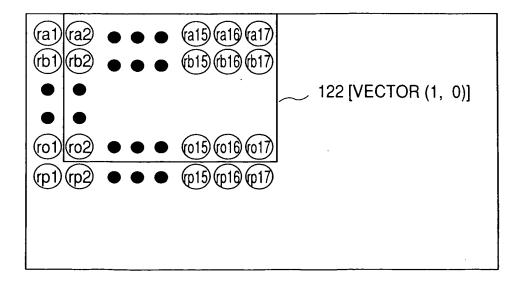


FIG. 13





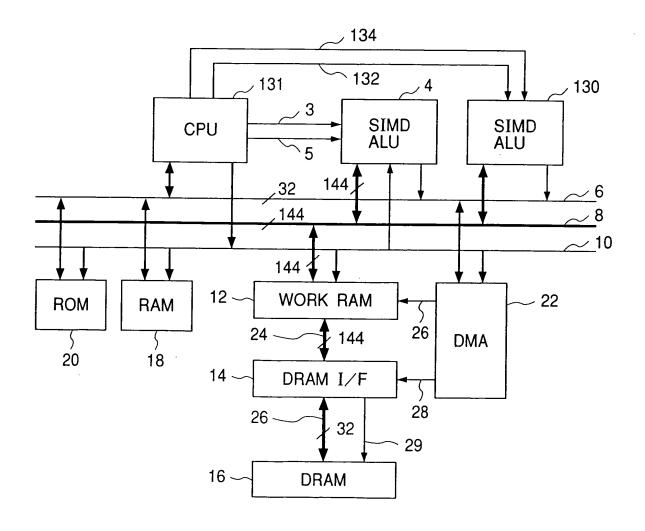
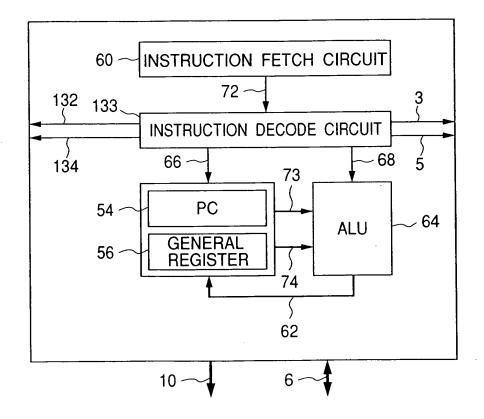


FIG. 16



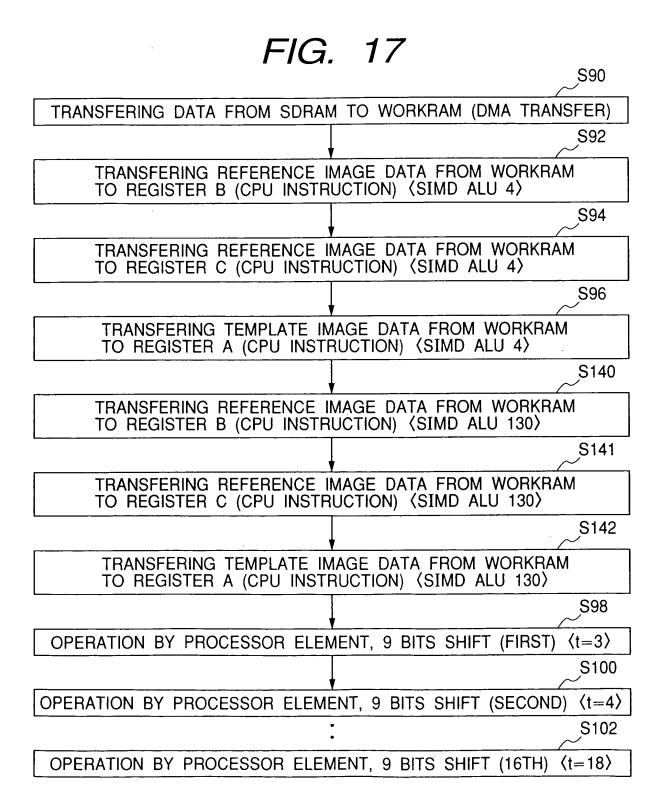
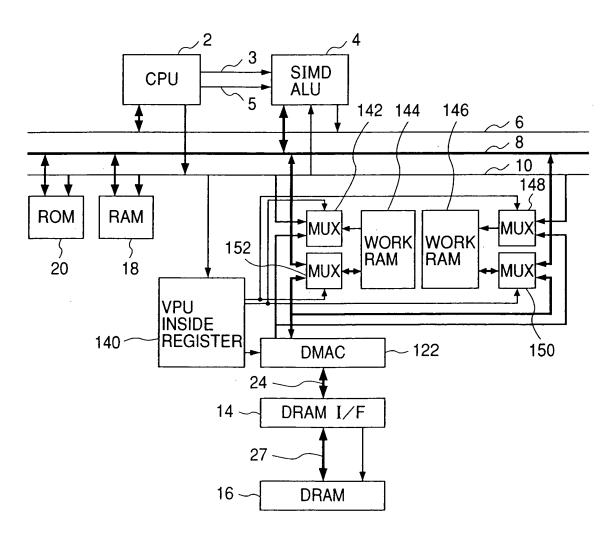


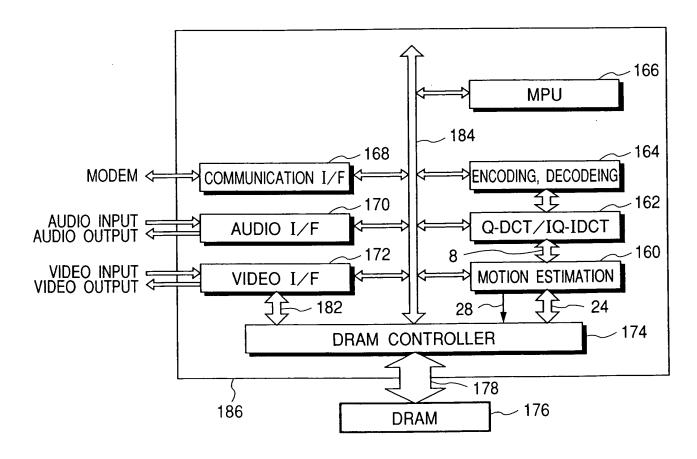
FIG. 18



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FIG. 19



16 / 16

FIG. 20

